Pao-Ann Hsiung's Publication List

Last Updated: October 9, 2013

(A) International Journal Papers

[1] **P.-A. Hsiung**, S.-J. Chen, T.-C. Hu, and S.-C. Wang, "PSM: An Object-Oriented Synthesis Approach to Multiprocessor System Design," *IEEE Transactions on VLSI Systems*, Vol. 4, No. 1, pp. 83-97, March 1996.

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[3] **P.-A. Hsiung**, C.-H. Chen, T.-Y. Lee, and S.-J. Chen, "ICOS: An Intelligent Concurrent Object-Oriented Synthesis Methodology for Multiprocessor Systems," *ACM Transactions on Design Automation of Electronic Systems*, Vol. 3, No. 2, pp. 109–135, April 1998.

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[5] **P.-A. Hsiung**, "Hardware-Software Timing Coverification of Concurrent Embedded Real-Time Systems," *IEE Proceedings on Computers and Digital Techniques*, Vol. 147, No. 2, pp. 81–90, March 2000.

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[11] F. Wang and **P.-A. Hsiung**, "Efficient and User-Friendly Verification," *IEEE Transactions on Computers*, Vol. 51, No. 1, pp. 61–83, January 2002.

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[13] **P.-A. Hsiung**, S.-W. Lin, C.-H. Tseng, T.-Y. Lee, J.-M. Fu, and W.-B. See, "VERTAF: An Application Framework for the Design and Verification of Embedded Real-Time Software," *IEEE Transactions on Software Engineering*, Vol. 30, No. 10, pp. 656–674, October 2004.

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(B) Books and Chapters

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