

Module 10

Basic VHDL

I. Introduction Section Questions

(The slides for these questions start at Slide 18.)

10.I.1. VHDL is a hardware description language that can be used to describe designs at various levels of abstractions. Which of the following system descriptions (from the Gajski and Kuhn Y-Chart) are directly supported by VHDL?

- a) Behavioral Model
- b) Structural Model
- c) Physical/Geometry Model
- d) All the above
- e) a and b only

10.I.2. The RASSP taxonomy of system representation uses five axes which represent Programmability, Structure, Function, Data Value, and

- a) logic value
- b) time
- c) testability
- d) algorithm

10.I.3. The VHDL language was developed under an Institute of Electrical and Electronic Engineers (IEEE) committee initiative as a standardized language for describing hardware and software.

- a) True
- b) False